Network Theorems

Dharmendra Kumar Pandey



Department of Physics, P. P. N. (PG) College, Kanpur – 208 001, U.P., India Email: <u>pandeydrdk@rediffmail.com</u>, <u>dr.dkpandey@gmail.com</u> Website: <u>dkpandey.weebly.com</u>

PPN College, Kanpur

Components



Active and Passive components

Passive component:

The electrical component through which energy is dissipated are called as passive component. e.g. resistor, inductor, and capacitor

Active component:

The circuit component which generates energy is called as active component. e.g. dc or ac sources etc.

Electrical Network

Electrical network/circuit:

The interconnection of electrical circuit components (resistors, capacitors, inductors and energy sources) which results a closed path is called as electrical network.

Active network:

An electrical circuit containing both the active and passive components is called as active network. **Passive network:**

An electrical circuit containing only passive components is called as passive network.

Linear and non-linear Electrical Network

Linear network:

If current in electrical circuit is directly proportional to the source voltage then the network is termed as linear network. i.e. there is linear relationship between current and voltage for this network.

Non-linear network:

If current in electrical circuit is not directly proportional to the source voltage then the network is termed as non-linear network. i.e. there is non-linear relationship between current and voltage for this network.

Four terminal electrical Network

Four terminal network: If an electrical network has two input and two output terminal then it is called as four terminal network. e.g. T-network or Y-network and π -network or ∇ -network.



Conversion of $T \leftrightarrow \pi$ Network

Zı ZB \mathbb{Z}_2 1 Z3 Zc ZA $Z_A = \frac{Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1}{Z_2}$ $Z_1 = \frac{Z_A Z_B}{Z_A + Z_B + Z_C}$ $Z_B = \frac{Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1}{Z_3}$ $Z_2 = \frac{Z_B Z_C}{Z_A + Z_B + Z_C}$ $Z_3 = \frac{Z_C Z_A}{Z_A + Z_R + Z_C}$ $\frac{Z_1 Z_2 + Z_2 \bar{Z}_3 + Z_3 Z_1}{Z_1}$ Z_C

Example



Out put of same circuit by software



Basic Law of electrical network

Ohm's Law:



Kirchoff's Current Law (KCL): The algebraic sum of currents at node in electrical network is equal to zero.



Incoming current –Out going current=0 Incoming current =Out going current

Kirchoff's Vltage law (KVL): The algebraic sum of instantaneous voltage drop across the circuit elements for a closed loop is zero.

$$\sum V = 0$$
 \longrightarrow $\sum iR - \sum E = 0$ \longrightarrow $\sum iR = \sum E$



Or

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Example for Basic Laws



Example for Basic Laws



Example for Basic Laws- Potential divider



Example for Basic Laws – Current control



Application of Kirchhoff's Law for two loop network



$$I_1 = \frac{E(Z_2 + Z_3)}{\left(Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1\right)} \qquad I_2 = \frac{Z_3 E}{\left(Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1\right)}$$

Mesh analysis of two loop Network



Here E_1 and E_2 are algebraic sum of e.m.f. of energy sources in mesh 1 and 2 respectively. Z_{11} and Z_{22} are called as **loop impedance** of mesh first and second respectively. Z_{12} and Z_{21} are called **as mutual impedance** of between meshes 1 and 2.



 E_1 Z_{11} $\begin{bmatrix} Z_{21} & E_2 \end{bmatrix}$ $\begin{bmatrix} Z_{11} & Z_{12} \end{bmatrix}$

Application of Mesh analysis for two loop Network



Thevenin's Theorem

Statement: The theorem states that "Any two terminal linear network containing energy sources and impedances (active network) is equivalent to a voltage source of E' in combination with impedance Z' in series. Where E' is open circuited voltage across the terminals of network and Z' is the impedance of network when the sources are replaced by their internal impedances or short circuited.



Thevenin's Theoremcontinued

If a load impedance Z_L is connected to the terminal A and B of the network then load current I_L can be written as,



Proof of Thevenin's Theorem



Find Load current using Thevenin's Theorem



Solution using Thevenin's Theorem



Norton's Theorem

Statement: The theorem states that "Any two terminal linear network containing energy sources and impedances (active network) is equivalent to a current source of I' in parallel combination with impedance Z'. Where I' is short circuited current through terminals of network and Z' is the impedance of network when the sources are replaced by their internal impedances or short circuited.



Norton's Theorem continued

If a load impedance Z_L is connected to the terminal A and B of the network then load current I_L can be written as,



Proof of Norton's Theorem



Proof of Norton's Theoremcontinued

If I' is short circuited current through terminals of network and Z' is the impedance of network when the sources are short circuited



Equivalence of Thevenin and Norton's Theorem

From *Thevenin* theorem, the load current can be written as, F'

$$I_L = \frac{E'}{Z' + Z_L}$$

If I' is shot circuited current through terminal of network, then



This is Nortons formula for Load current I_L . Therefore both the theorem are equivalent to each other.

Find load current using Norton's Theorem



Solution using Norton's Theorem



Superposition Theorem

Statement: The theorem states that "In any linear network containing impedances and energy sources (active linear network), the current in any element or branch or mesh is equal to algebraic sum of currents that would separately flow in that by each source while other sources are replaced by their internal impedances".

$$I = \sum I_{\chi}$$
; x = 1,2,....,n

Here n is number of energy sources in the network. If a two mesh active linear network has two energy sources and $I_1 \& I_2$ are the currents in mesh1 and mesh 2 then,

$$I_1 = I_1' + I_1'' \qquad \qquad I_2 = I_2' + I_2''$$

Where I_1' and I_2' are currents in mesh1 and mesh 2 by first energy source while I_1'' and I_2'' are currents in mesh1 and mesh 2 by second energy source.

Superposition Theorem



Example of Superposition



A Lot of Thanks for kind attention